IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Kannan Raj et al.

Group Art Unit:

Serial No.: 09/839,023

Examiner:

OCT 0 8 2003 Technology Center 2600

Filed:

April 20, 2001

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Atty. Dkt. No.:

ITL.0462US

(P9816)

For:

Optically Interconnecting

Multiple Processors

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicant submits the references listed on the attached form PTO 1449, copies of which are enclosed. A copy of a communication dated September 24, 2003 from a foreign patent office in a counterpart application is also enclosed. I, the undersigned, hereby certify that each item of information contained in the Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement. Please apply any charges or credits to Deposit Account No. 20-1504 (ITL.0462US).

Respectfully submitted,

September 30, 2003

Date

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I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450,

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Monica Jacobs

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INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)					ITL.0462US (P9816) 09 APPLICANT(S):		09/839,023	9/839,023	
					Kannan Raj et al.				
(Use several sheets if necessary)					FILING DATE:	GRO	OUP ART UNIT:	0/22	
April 20, 2001 April 20								2633	
U.S. PATENT DOCUMENTS *EXAMINER POSITIVE PROPERTY NUMBER PATE NAME CLASS SUBSIASS FILING DATE									
INITIAL		DOCUMENT NUMBER	DATE		NAME	CLASS	SUBCLASS	IF APPROPRIATE	
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	C.					PCT 0	8 2003		
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	Q.								
	R.								
	S.								
EXAMINER	Dal				DATE CONSIDERED 04/201				
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PTO-1449